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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**In re the Application of: **Madan**Docket Number: **T1-34845**Serial No.: **10/717,146**Art Unit: **2818**Filed: **11/18/2003**Examiner: **S.L. Mai**For: **BITLINE PRECHARGE TIMING SCHEME TO IMPROVE SIGNAL MARGIN****CERTIFICATION OF FACSIMILE TRANSMISSION**

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January 10, 2005  
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**FACSIMILE COVER SHEET**

<input checked="" type="checkbox"/> <b>FACSIMILE COVER SHEET</b>	<input checked="" type="checkbox"/> <b>AMENDMENT (13 Pages)</b>
<input type="checkbox"/> NEW APPLICATION	EOT (# Page)
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<input type="checkbox"/> DIVISIONAL APP'N	
<b>NAME OF INVENTOR(S):</b> <i>Madan</i>	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>10/717,146</b>	
<b>FILING DATE:</b> <b>November 18, 2003</b>	
<b>TITLE OF INVENTION:</b> <b>BITLINE PRECHARGE TIMING SCHEME TO IMPROVE SIGNAL MARGIN</b>	
<b>TI FILE NO.:</b> <b>T1-34845</b> <b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>	
<b>DATE FAXED:</b> <b>January 10, 2005</b>	
<b>DUE:</b> <b>January 10, 2005</b>	
<b>ATTY/SECY:</b> <b>Robert N. Rountree</b>	

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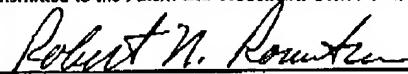
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**In re the Application of: **Madan**Attorney Docket No.: **TI-34845**Serial No.: **10/717,146**Examiner: **Mai, Son Luu**Filed: **November 18, 2003**Art Unit: **2818**For: **BITLINE PRECHARGE TIMING SCHEME TO IMPROVE SIGNAL MARGIN****AMENDMENT UNDER 37 C.F.R. § 1.111**

December 30, 2003

Honorable Commissioner of  
Patents and Trademarks

Washington, D.C. 20231

I hereby certify that the above correspondence is being facsimile transmitted to the Patent and Trademark Office on December 30, 2003.

  
\_\_\_\_\_  
Robert N. Rountree, Reg. No. 39,347

Dear Sir:

Reconsideration is requested in response to Examiner's rejection dated October 8, 2004. Please amend the instant application as follows.

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